



ESD



TVS



MOS



LDO



Diode



Sensor



DC-DC

Product Specification

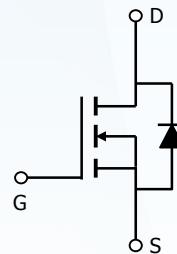
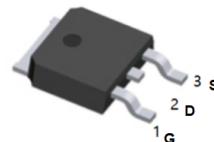
▶ Domestic Part Number	AOD4132
▶ Overseas Part Number	AOD4132
▶ Equivalent Part Number	AOD4132



EV is the abbreviation of name EVVO

30V N-Channel MOSFET
Description

The AOD4132 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and low gate resistance. This device is ideally suited for use as a low side switch in CPU core power conversion.


Features
 $V_{DS} (V) = 30V$
 $I_D = 85A (V_{GS} = 10V)$
 $R_{DS(ON)} < 4m\Omega (V_{GS} = 10V)$
 $R_{DS(ON)} < 6m\Omega (V_{GS} = 4.5V)$

TO-252(DPAK) top view
Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^{B,G}	I_D	85	A
$T_C=100^\circ C$ ^B	I_D	63	
Pulsed Drain Current	I_{DM}	200	
Avalanche Current ^C	I_{AR}	30	A
Repetitive avalanche energy $L=0.1mH$ ^C	E_{AR}	112	mJ
Power Dissipation ^B	P_D	100	W
$T_C=100^\circ C$	P_D	50	
Power Dissipation ^A	P_{DSM}	2.5	W
$T_A=70^\circ C$	P_{DSM}	1.6	
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 175	°C

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$R_{\theta JA}$	14.2	20	°C/W
Maximum Junction-to-Ambient ^A		39	50	°C/W
Maximum Junction-to-Case ^C	$R_{\theta JC}$	0.8	1.5	°C/W

30V N-Channel MOSFET

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D=250\mu\text{A}, V_{\text{GS}}=0\text{V}$	30			V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{DS}}=24\text{V}, V_{\text{GS}}=0\text{V}$		1		
			$T_J=55^\circ\text{C}$		5	μA
I_{GSS}	Gate-Body leakage current	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}= \pm 20\text{V}$			100	nA
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	1	1.8	3	V
$I_{\text{D(ON)}}$	On state drain current	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=5\text{V}$	85			A
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}, I_D=20\text{A}$		2.8	4	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=20\text{A}$		4.4	6	$\text{m}\Omega$
g_{FS}	Forward Transconductance	$V_{\text{DS}}=5\text{V}, I_D=20\text{A}$		106		S
V_{SD}	Diode Forward Voltage	$I_S=1\text{A}, V_{\text{GS}}=0\text{V}$		0.72	1	V
I_S	Maximum Body-Diode Continuous Current				85	A
DYNAMIC PARAMETERS						
C_{iss}	Input Capacitance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=15\text{V}, f=1\text{MHz}$		3700	4400	pF
C_{oss}	Output Capacitance			700		pF
C_{rss}	Reverse Transfer Capacitance			390		pF
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, f=1\text{MHz}$		0.54	0.7	Ω
SWITCHING PARAMETERS						
$Q_g(10\text{V})$	Total Gate Charge	$V_{\text{GS}}=4.5\text{V}, V_{\text{DS}}=15\text{V}, I_D=20\text{A}$		63	76	nC
$Q_g(4.5\text{V})$	Total Gate Charge			33	40	nC
Q_{gs}	Gate Source Charge			8.6		nC
Q_{gd}	Gate Drain Charge			17.6		nC
$t_{\text{D(on)}}$	Turn-On Delay Time	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=15\text{V}, R_L=0.75\Omega, R_{\text{GEN}}=3\Omega$		12		ns
t_r	Turn-On Rise Time			15.5		ns
$t_{\text{D(off)}}$	Turn-Off Delay Time			40		ns
t_f	Turn-Off Fall Time			14		ns
t_{rr}	Body Diode Reverse Recovery Time	$I_F=20\text{A}, dI/dt=100\text{A}/\mu\text{s}$		34	41	ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F=20\text{A}, dI/dt=100\text{A}/\mu\text{s}$		30		nC

A: The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The Power dissipation P_{DSM} is based on steady-state $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design, and the maximum temperature to 175°C may be used if the PCB or heatsink allows it.

B. The power dissipation P_D is based on $T_{J(\text{MAX})}=175^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

C: Repetitive rating, pulse width limited by junction temperature $T_{J(\text{MAX})}=175^\circ\text{C}$.

D. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JC}$ and case to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.

F. These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with $T_A=25^\circ\text{C}$. The SOA curve provides a single pulse rating.

G. The maximum current rating is limited by the package current capability.

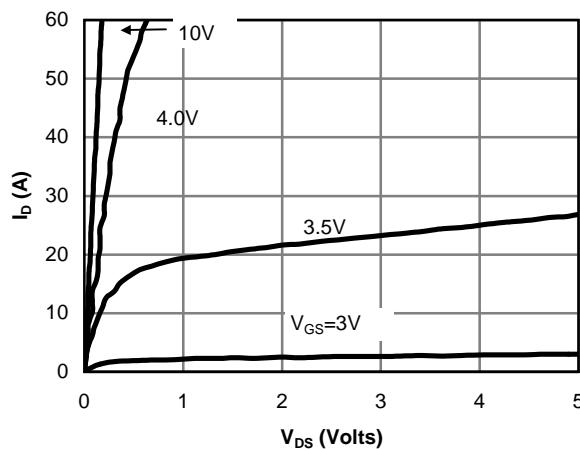
30V N-Channel MOSFET
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS


Fig 1: On-Region Characteristics

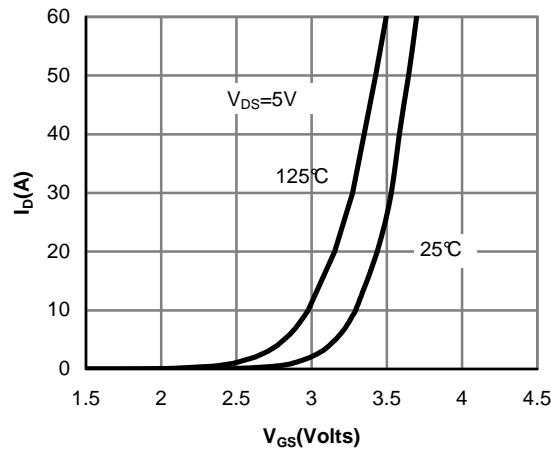


Figure 2: Transfer Characteristics

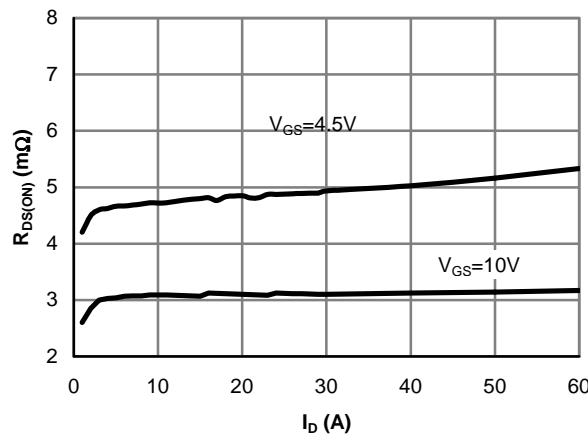


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

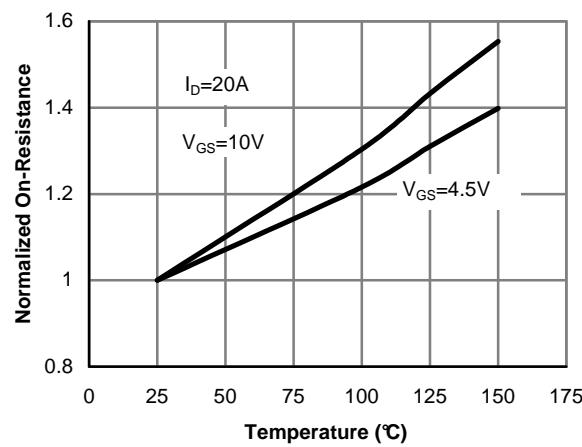


Figure 4: On-Resistance vs. Junction Temperature

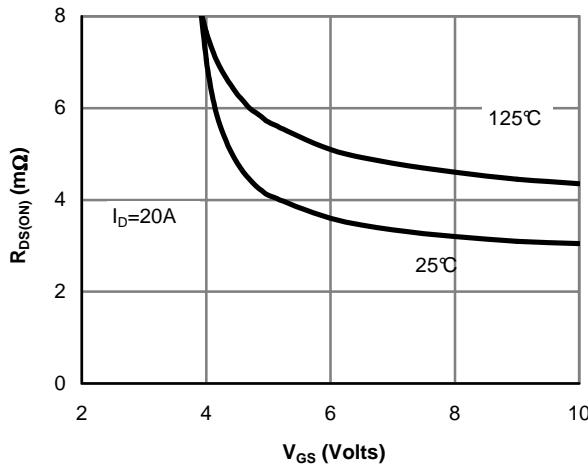


Figure 5: On-Resistance vs. Gate-Source Voltage

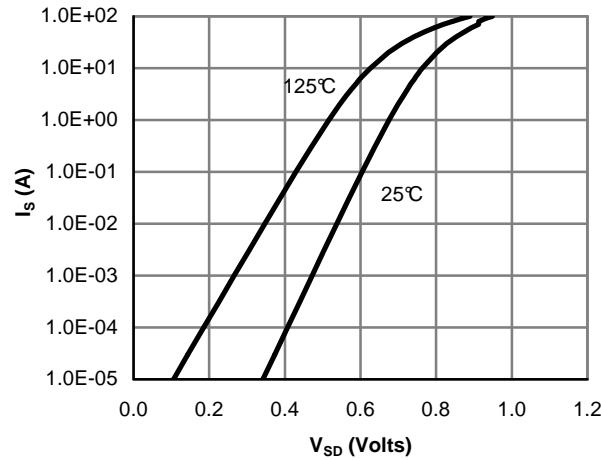


Figure 6: Body-Diode Characteristics

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

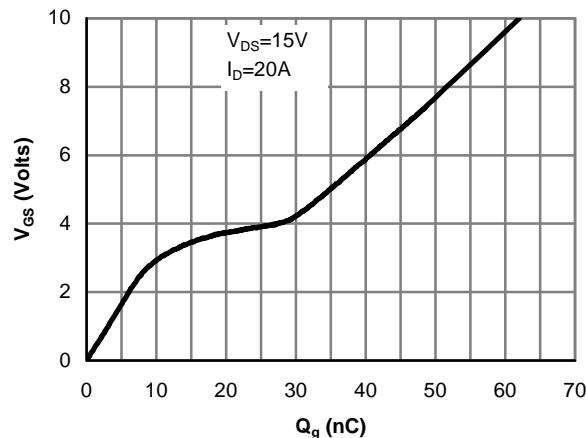


Figure 7: Gate-Charge Characteristics

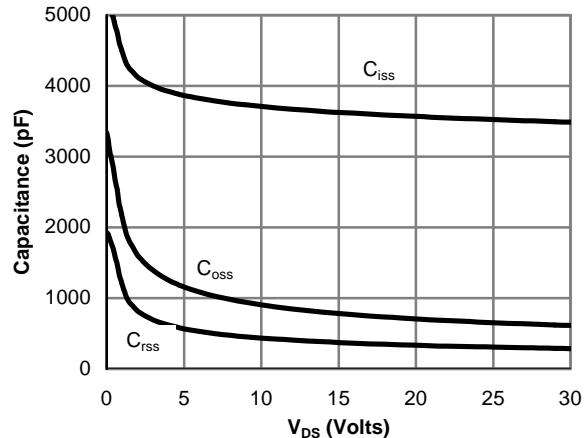


Figure 8: Capacitance Characteristics

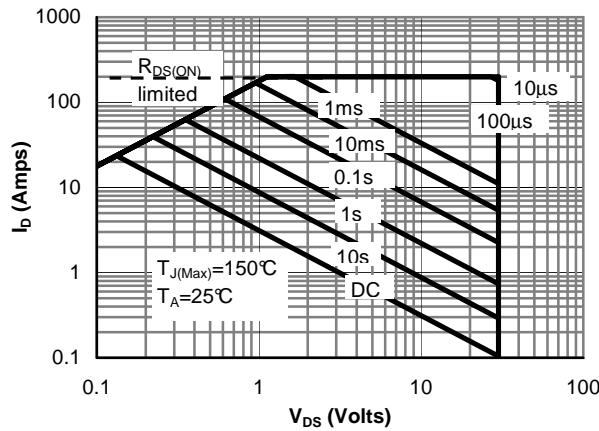


Figure 9: Maximum Forward Biased Safe Operating Area (Note F)

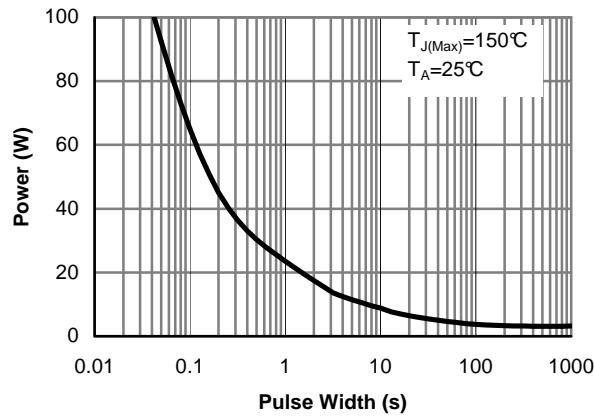


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note F)

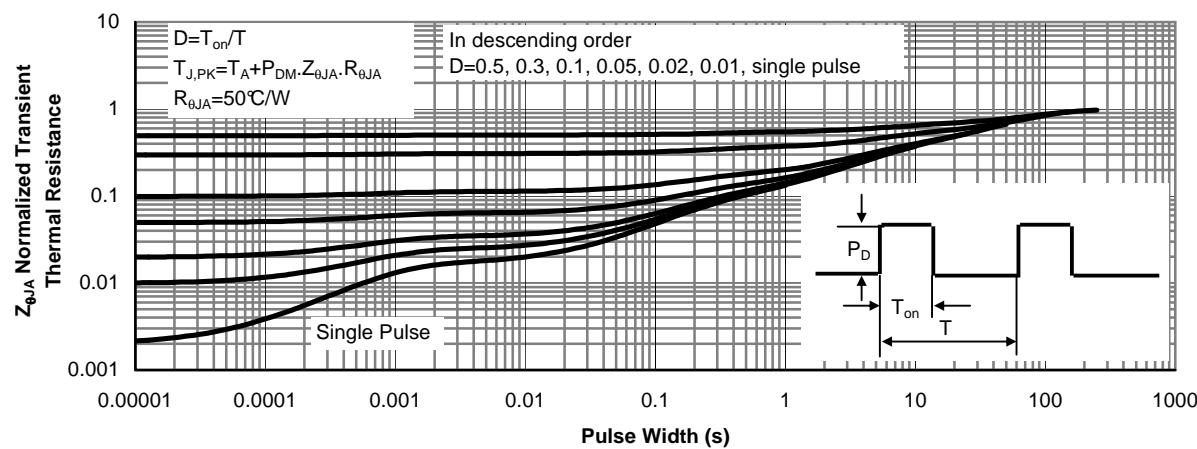


Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

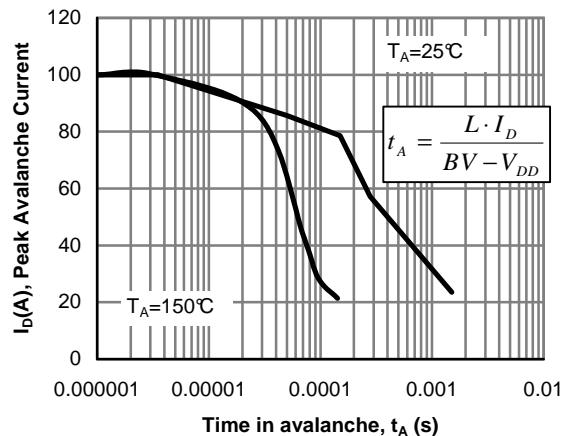
30V N-Channel MOSFET
TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS


Figure 12: Single Pulse Avalanche capability

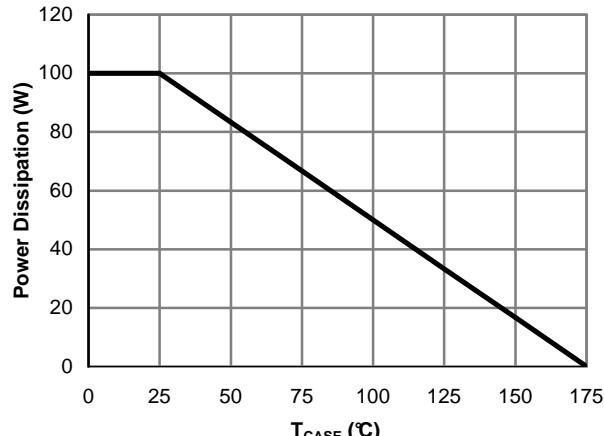


Figure 13: Power De-rating (Note B)

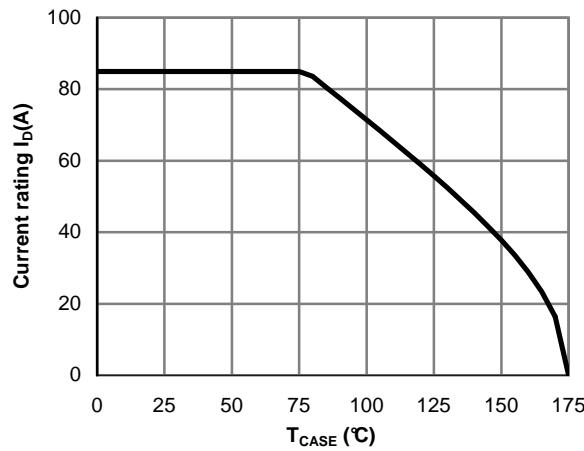
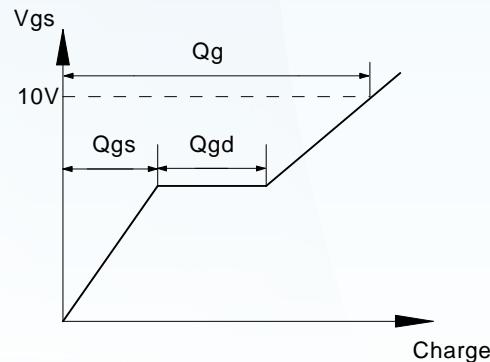
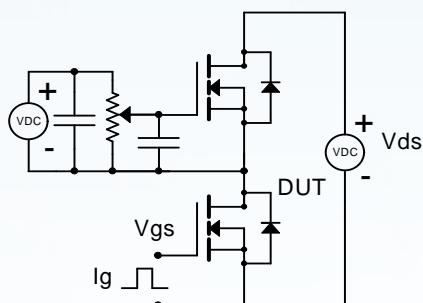


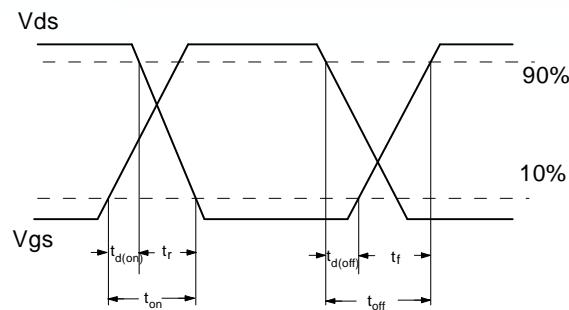
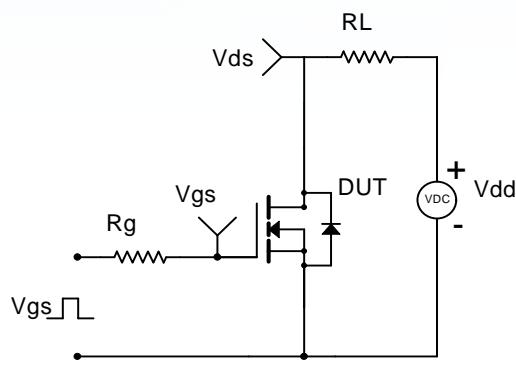
Figure 14: Current De-rating (Note B)

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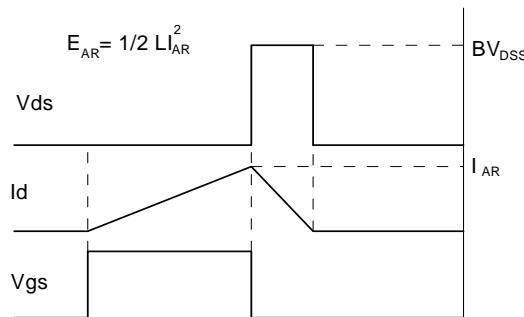
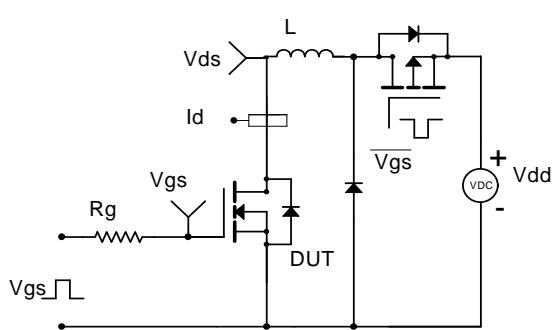
Gate Charge Test Circuit & Waveform



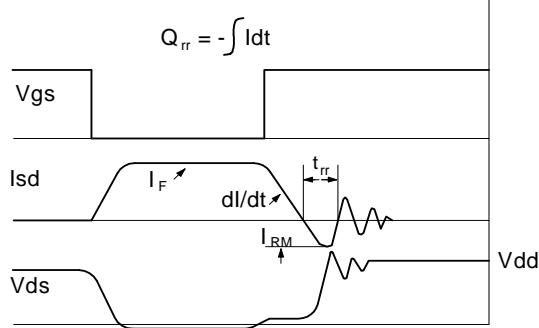
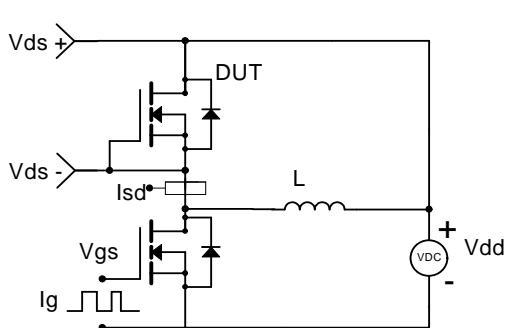
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching (UIS) Test Circuit & Waveforms

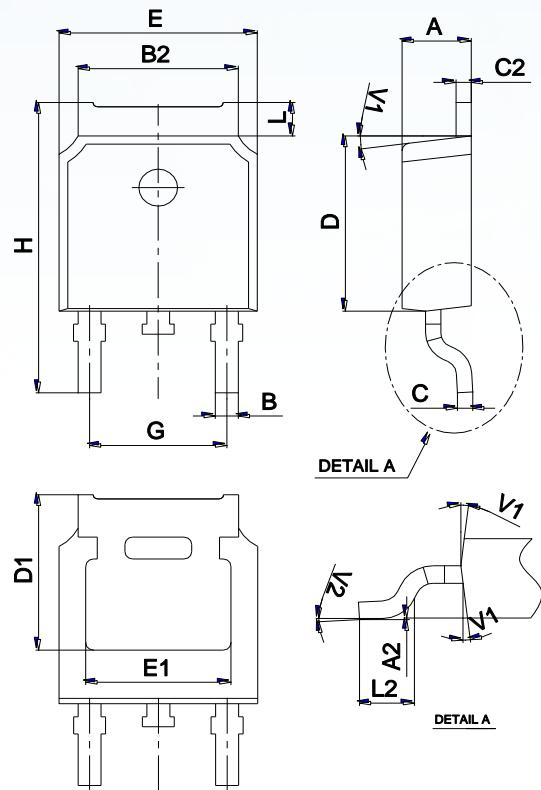


Diode Recovery Test Circuit & Waveforms



30V N-Channel MOSFET

Package Mechanical Data TO-252



Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	2.10		2.50	0.083		0.098
A2	0		0.10	0		0.004
B	0.66		0.86	0.026		0.034
B2	5.18		5.48	0.202		0.216
C	0.40		0.60	0.016		0.024
C2	0.44		0.58	0.017		0.023
D	5.90		6.30	0.232		0.248
D1	5.30REF			0.209REF		
E	6.40		6.80	0.252		0.268
E1	4.63			0.182		
G	4.47		4.67	0.176		0.184
H	9.50		10.70	0.374		0.421
L	1.09		1.21	0.043		0.048
L2	1.35		1.65	0.053		0.065
V1		7°			7°	
V2	0°		6°	0°		6°

Ordering information

Order code	Package	Baseqty	Delivery mode
AOD4132	TO-252	2500	Tape and reel

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