















**ESD** 

TVS

MOS

LDO

Diode

Sensor

DC-DC

# **Product Specification**

Domestic Part Number	FDN339AN
Overseas Part Number	FDN339AN-EV
▶ Equivalent Part Number	FDN339AN





#### **General Description**

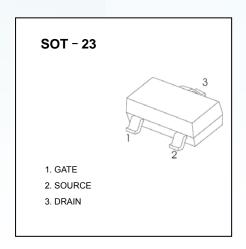
This N-Channel 2.5V specified MOSFET is has been especially tailored to minimize the on-state resistance and yet maintain low gate charge for superior switching performance.

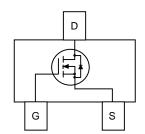
#### **Applications**

- DC/DC converter
- Load switch

#### **Features**

- VDS (V) = 20V
- lacktriangle RDS(ON) <38m  $\Omega$  (V GS = 4.5V)
- RDS(ON) <50m  $\Omega$  (V GS = 2.5V)





Absolute Maximum Ratings T<sub>A</sub> = 25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		20	V
V <sub>GSS</sub>	Gate-Source Voltage		±8	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1a)	3	А
	- Pulsed		20	
P <sub>D</sub>	Power Dissipation for Single Operation	(Note 1a)	0.5	W
		(Note 1b)	0.46	
$T_J$ , $T_{stg}$	Operating and Storage Junction Temperature Range		-55 to +150	°C
Therma	I Characteristics			
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	250	°C/W
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	(Note 1)	75	°C/W



Electrical Characteristics T <sub>A</sub> =	25°C unless otherwise noted
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Symbol	Parameter	Test Conditions	Min	Тур	Max	Units	
Off Char	acteristics						
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	20			V	
$\frac{\Delta BV_{DSS}}{\Delta T_{J}}$	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}, \text{Referenced to } 25^{\circ}\text{C}$		14		mV/°C	
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	V <sub>DS</sub> = 16 V, V <sub>GS</sub> = 0 V			1	μΑ	
I <sub>GSSF</sub>	Gate-Body Leakage Current, Forward	V <sub>GS</sub> = 8 V, V <sub>DS</sub> = 0 V			100	nA	
I <sub>GSSR</sub>	Gate-Body Leakage Current, Reverse	$V_{GS} = -8 \text{ V}, V_{DS} = 0 \text{ V}$			-100	nA	
On Char	acteristics (Note 2)						
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu A$	0.4	0.85	1.5	V	
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}, \text{Referenced to } 25^{\circ}\text{C}$		-3		mV/°C	
R <sub>DS(on)</sub>	Static Drain-Source	$V_{GS} = 4.5 \text{ V}, I_D = 3 \text{ A}$		29	38	10	
	On-Resistance	$V_{GS} = 2.5 \text{ V}, I_D = 2.4 \text{ A}$		39	50	mΩ	
I <sub>D(on)</sub>	On-State Drain Current	V <sub>GS</sub> = 4.5 V, V <sub>DS</sub> = 5 V	10			Α	
<b>g</b> FS	Forward Transconductance	$V_{DS} = 5 \text{ V}, I_{D} = 3 \text{ A}$		11		S	
Dynamic	Characteristics	!					
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V},$		700		pF	
Coss	Output Capacitance	f = 1.0 MHz		175		pF	
Crss	Reverse Transfer Capacitance	1		85		pF	
Switchin	g Characteristics (Note 2)						
t <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> = 10 V, I <sub>D</sub> = 1 A,		8	16	ns	
t <sub>r</sub>	Turn-On Rise Time	$V_{GS}$ = 4.5 V, $R_{GEN}$ = 6 $\Omega$		10	18	ns	
t <sub>d(off)</sub>	Turn-Off Delay Time	†		18	29	ns	
t <sub>f</sub>	Turn-Off Fall Time	†		5	10	ns	
Qq	Total Gate Charge	$V_{DS} = 10 \text{ V}, I_{D} = 3 \text{ A},$		7	10	nC	
Q <sub>gs</sub>	Gate-Source Charge	$V_{GS} = 4.5 \text{ V}$		1.2		nC	
Q <sub>gd</sub>	Gate-Drain Charge	†		1.9		nC	
Drain-Sc	ource Diode Characteristics a	nd Maximum Ratings					
Is	Maximum Continuous Drain-Source D				0.42	Α	
V <sub>SD</sub>	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_S = 0.42 \text{ A}$ (Note 2)		0.65	1.2	V	

#### Notes:

1:  $R_{\text{BJA}}$  is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins.  $R_{\text{BJC}}$  is guaranteed by design while  $R_{\text{BCA}}$  is determined by the user's board design.



a) 250°C/W when mounted on a 0.02 in² Pad of 2 oz. Cu.



b) 270°C/W on a minimum mounting pad of 2 oz. Cu.

Scale 1 : 1 on letter size paper

2: Pulse Test: Pulse Width  $\leq 300~\mu s$ , Duty Cycle  $\leq 2.0\%$ 



## **Typical Characteristics**

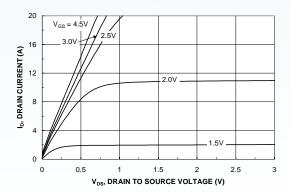


Figure 1. On-Region Characteristics.

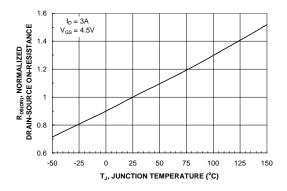


Figure 3. On-Resistance Variation with Temperature.

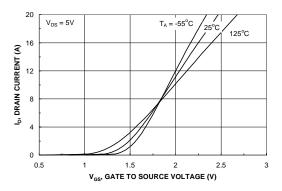


Figure 5. Transfer Characteristics.

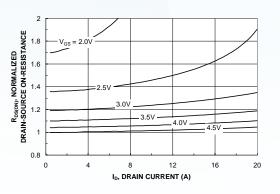


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

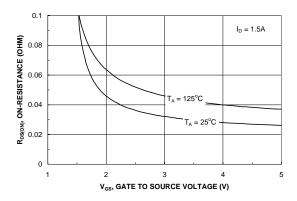


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

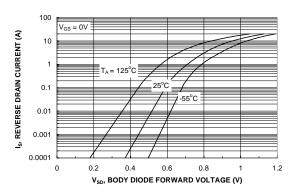
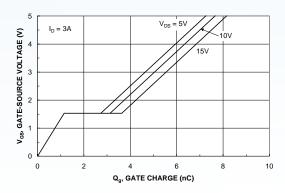


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.



#### Typical Characteristics (continued)



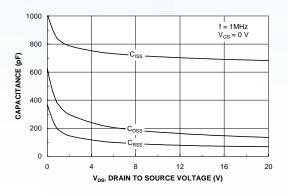
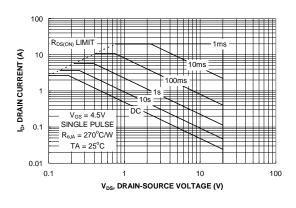


Figure 7. Gate Charge Characteristics.

Figure 8. Capacitance Characteristics.



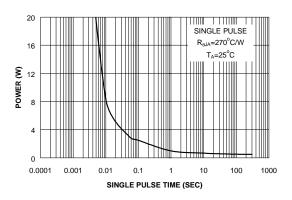


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

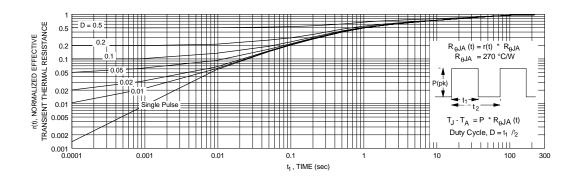
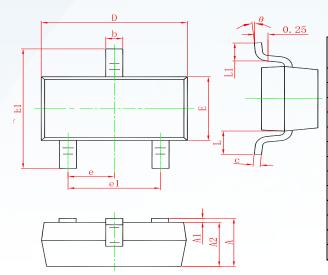


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in Note 1b. Transient themal response will change depending on the circuit board design.

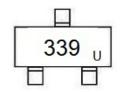


#### **SOT-23 PACKAGE OUTLINE DIMENSIONS**



Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
А	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950 TYP.		0.037	TYP.	
e1	1.800	2.000	0.071	0.079	
L	0.550 REF.		0.022	REF.	
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

# Marking



## **Ordering information**

Order code	Package	Baseqty	Deliverymode
FDN339AN	SOT-23	3000	Tape and reel



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