















ESD

TVS

MOS

LDO

Diode

Sensor

DC-DC

Product Specification

Domestic Part Number	AO3442A
Overseas Part Number	AO3442A
▶ Equivalent Part Number	AO3442A





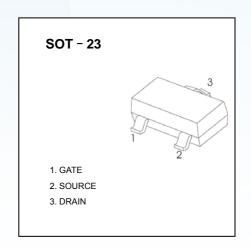
100V N-Channel MOSFET

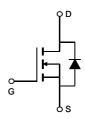
General Description

The AO3442 combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{\text{DS(ON)}}$. This device is ideal for boost converters and synchronous rectifiers for consumer, telecom, industrial power supplies and LED backlighting.

Product Summary

V_{DS}	100V
I_D (at V_{GS} =10V)	1A
R _{DS(ON)} (at V _{GS} =10V)	< 630m Ω
$R_{DS(ON)}$ (at V_{GS} =4.5V)	$<$ 720m Ω





Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V _{DS}	100	V
Gate-Source Voltage		V _{GS}	±20	V
Continuous Drain Current	T _A =25℃		1	
	T _A =70℃	'D	0.8	A
Pulsed Drain Current	Ċ	I _{DM}	4	
	T _A =25℃	Р	1.4	W
Power Dissipation ^B	T _A =70℃	P _D	0.9	VV
Junction and Storage	Temperature Range	T _J , T _{STG}	-55 to 150	C

Thermal Characteristics						
Parameter		Symbol	Тур	Max	Units	
Maximum Junction-to-Ambient A	t ≤ 10s	D	70	90	℃/W	
Maximum Junction-to-Ambient AD	Steady-State	$R_{\theta JA}$	100	125	°C/W	
Maximum Junction-to-Lead	Steady-State	$R_{\theta JL}$	63	80	€/M	



Electrical Characteristics (T_J=25℃ unless otherwise noted)

Symbol	Parameter	Conditions		Min	Тур	Max	Units
STATIC PARAMETERS							
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0V		100			V
I _{DSS}	Zero Gate Voltage Drain Current	V_{DS} =100V, V_{GS} =0V				1	μА
·DSS			T _J =55℃			5	μπ
I_{GSS}	Gate-Body leakage current	V_{DS} =0V, V_{GS} =±20V				±100	nA
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$		1.7	2.3	2.9	V
I _{D(ON)}	On state drain current	V _{GS} =10V, V _{DS} =5V		4			Α
		V_{GS} =10V, I_D =1A			514	630	mΩ
R _{DS(ON)}	Static Drain-Source On-Resistance		T _J =125℃		983	1200	11122
		V_{GS} =4.5V, I_{D} =0.8A			554	720	mΩ
g _{FS}	Forward Transconductance	$V_{DS}=5V$, $I_{D}=1A$			2.8		S
V_{SD}	Diode Forward Voltage	I _S =1A,V _{GS} =0V			0.9	1.2	V
I _S	Maximum Body-Diode Continuous Curr	Body-Diode Continuous Current ^G				1	Α
DYNAMIC	PARAMETERS						
C _{iss}	Input Capacitance	V_{GS} =0V, V_{DS} =50V, f=1MHz V_{GS} =0V, V_{DS} =0V, f=1MHz			100		pF
Coss	Output Capacitance				13		pF
C _{rss}	Reverse Transfer Capacitance				5		pF
R_g	Gate resistance			2.5	5	7.5	Ω
SWITCHI	NG PARAMETERS						
Q _g (10V)	Total Gate Charge	V _{GS} =10V, V _{DS} =50V, I _D =1A			2.8	6	nC
Q _g (4.5V)	Total Gate Charge				1.5	3	nC
Q_{gs}	Gate Source Charge				0.4		nC
Q_{gd}	Gate Drain Charge				0.8		nC
t _{D(on)}	Turn-On DelayTime	V_{GS} =10V, V_{DS} =50V, R_L =50 Ω , R_{GEN} =3 Ω			5		ns
t _r	Turn-On Rise Time				4		ns
t _{D(off)}	Turn-Off DelayTime				12		ns
t _f	Turn-Off Fall Time				5		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =5.6A, dI/dt=100A/μs			52		ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =5.6A, dI/dt=100A/j	us		60		nC

A. The value of $R_{\theta JA}$ is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, in a still air environment with T_A =25° C. The value in any given application depends on the user's specific board design. B. The power dissipation P_D is based on $T_{J(MAX)}=150^\circ$ C, using \leqslant 10s junction-to-ambient thermal resistance. C. Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150^\circ$ C. Ratings are based on low frequency and duty cycles to keep

initialT_J=25° C.

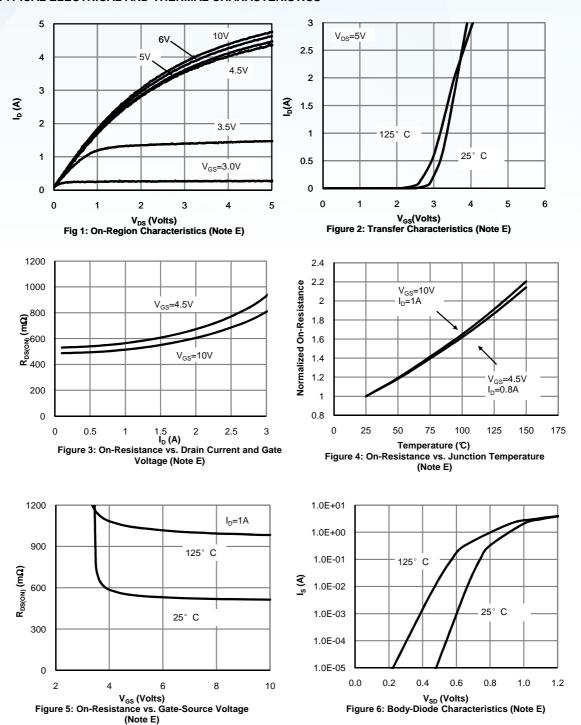
D. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to lead $R_{\theta JL}$ and lead to ambient.

E. The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

F. These curves are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, assuming a maximum junction temperature of T_{J(MAX)}=150° C. The SOA curve provides a single pulse rating.

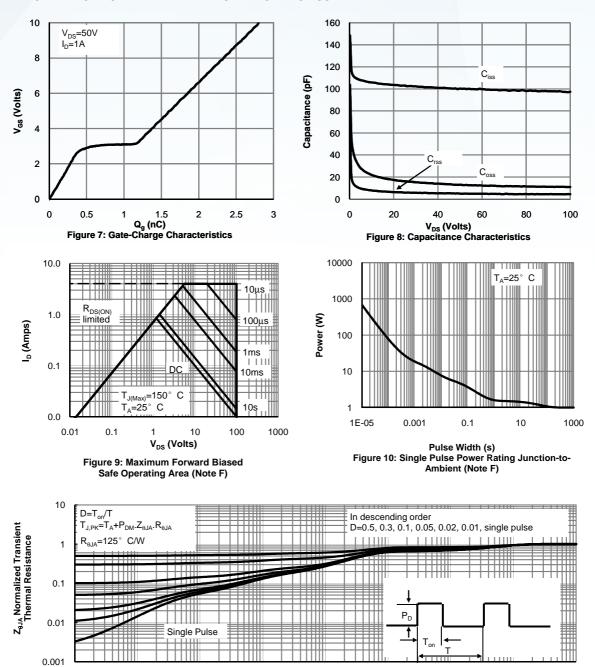


TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS





TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS



Pulse Width (s)
Figure 11: Normalized Maximum Transient Thermal Impedance (Note F)

0.1

10

100

1000

0.01

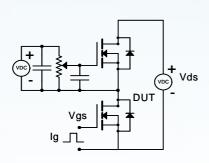
1E-05

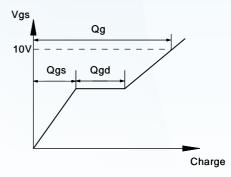
0.0001

0.001

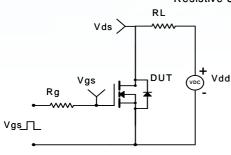


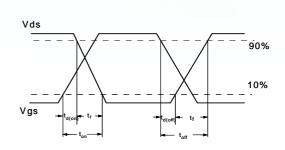
Gate Charge Test Circuit & Waveform



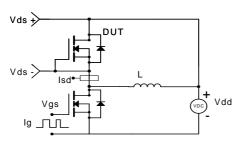


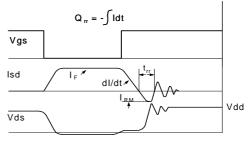
Resistive Switching Test Circuit & Waveforms





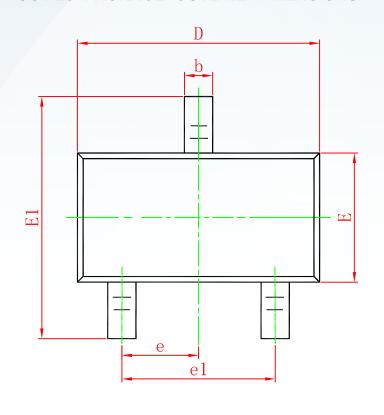
Diode Recovery Test Circuit & Waveforms

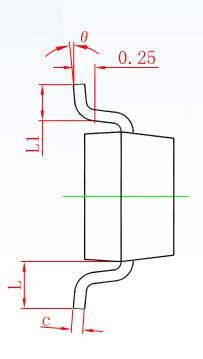


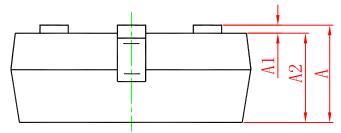




SOT-23 PACKAGE OUTLINE DIMENSIONS







Symbol	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950	TYP.	0.037	TYP.	
e1	1.800	2.000	0.071	0.079	
L	0.550 REF.		0.022	REF.	
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	



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