















ESD

TVS

MOS

LDO

Diode

Sensor

DC-DC

Product Specification

Domestic Part Number	IRF7842
Overseas Part Number	IRF7842
▶ Equivalent Part Number	IRF7842





Features

- Advanced Trench MOS Technology
- Low Gate Charge
- Fast Switching Speed
- 100% EAS Guaranteed
- Green Device Available

Applications

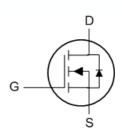
- Power Management in Desktop Computer or DC/DC Converters.
- Isolated DC/DC Converters in Telecom and Industrial.

Product Summary

BVDSS	RDSON	ID
40V	3.2mΩ	24A

SOP8 Pin Configuration





Absolute Maximum Ratings

Symbol	Parameter	Rating	Units	
V _{DS}	Drain-Source Voltage	40	V	
V _{GS}	Gate-Source Voltage	±20	V	
I _D @T _A =25°C	Continuous Drain Current, V _{GS} @ 10V ¹	24	Α	
I _D @T _A =70°C	Continuous Drain Current, V _{GS} @ 10V ¹	19	Α	
I _{DM}	Pulsed Drain Current ²	170	Α	
EAS	Single Pulse Avalanche Energy ³	145	mJ	
I _{AS}	Avalanche Current	54	Α	
P _D @T _A =25°C	Total Power Dissipation ⁴	22	W	
T _{STG}	Storage Temperature Range	-55 to 150	°C	
TJ	Operating Junction Temperature Range	-55 to 150	°C	

Thermal Data

Symbol	Parameter	Тур.	Max.	Unit
RθJA	Thermal Resistance Junction-Ambient ¹		75	°C/W
R _{θJA}	Thermal Resistance Junction-Ambient 1 , $t \leq 10$ sec		40	°C/W



N-Channel Electrical Characteristics (T_J=25 °C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V , I _D =250uA	40			V
D	Static Drain-Source On-Resistance ²	V_{GS} =10V , I_D =20A		2.5	3.2	mΩ
R _{DS(ON)}		V _{GS} =4.5V , I _D =15A		3.8	5.3	
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.7	2.2	٧
lane	Drain-Source Leakage Current	V _{DS} =40V , V _{GS} =0V , T _J =25°C			1	uA
IDSS		V _{DS} =40V , V _{GS} =0V , T _J =55°C			5	
Igss	Gate-Source Leakage Current	V _{GS} =±20V , V _{DS} =0V			±100	nA
gfs	Forward Transconductance	V _{DS} =5V , I _D =20A		75		8
R_g	Gate Resistance	V _{DS} =0V , V _{GS} =0V , f=1MHz		1.5		Ω
Q_g	Total Gate Charge (4.5V)			22.7		
Q _{gs}	Gate-Source Charge	V _{DS} =20V , V _{GS} =4.5V , I _D =20A		7.5		nC
Q _{gd}	Gate-Drain Charge			5.5		1
$T_{d(on)}$	Turn-On Delay Time	V_{DD} =20V , V_{GS} =10V , R_{G} =3 Ω		10		
Tr	Rise Time			5		no
T _{d(off)}	Turn-Off Delay Time			33		ns
Tf	Fall Time			6.5		
Ciss	Input Capacitance	V _{DS} =20V , V _{GS} =0V , f=1MHz		2648		
Coss	Output Capacitance			899		рF
Crss	Reverse Transfer Capacitance			71		

Diode Characteristics

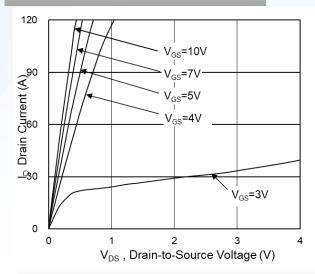
Symbol	Parameter	Conditions	Min.	Тур.	Max.	Unit
ls	Continuous Source Current ^{1,5}	V _G =V _D =0V , Force Current			30	Α
V_{SD}	Diode Forward Voltage ²	V _{GS} =0V , I _S =1A , T _J =25°C			1	V

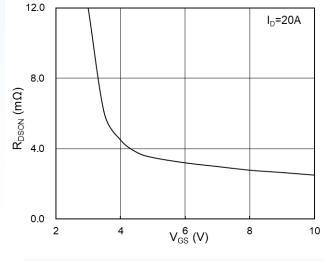
Note:

- 1. The data tested by surface mounted on a 1 inch² FR-4 board with 2OZ copper.
- 2.The data tested by pulsed , pulse width $\,\leq\,300\text{us}$, duty cycle $\,\leq\,2\%$
- 3. The EAS data shows Max. rating . The test condition is V_{DD} =25V, V_{GS} =10V, L=0.1mH, I_{AS} =54A
- 4. The power dissipation is limited by 150°C junction temperature
- 5. The data is theoretically the same as I_D and I_{DM} , in real applications, should be limited by total power dissipation.

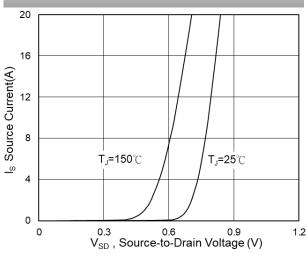


N-Channel Typical Characteristics









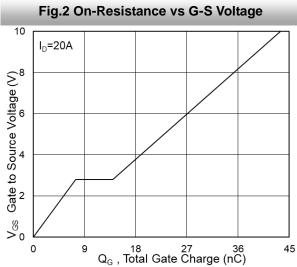
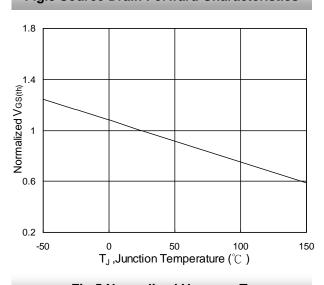
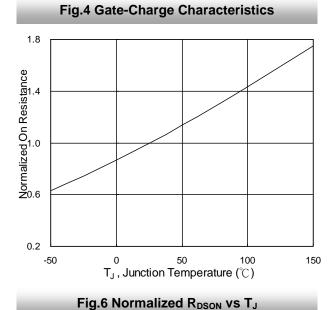
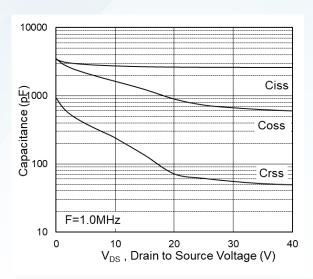


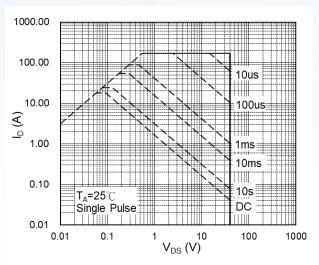
Fig.3 Source Drain Forward Characteristics











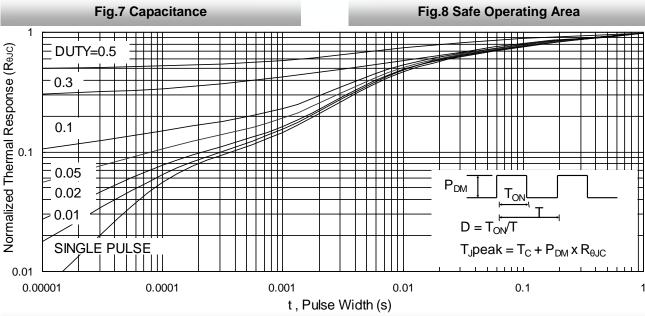
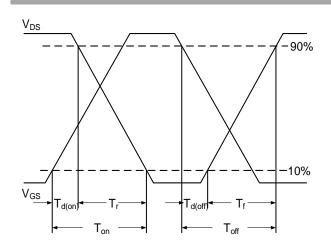
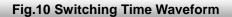


Fig.9 Normalized Maximum Transient Thermal Impedance





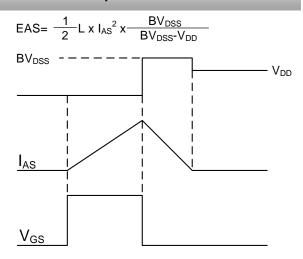


Fig.11 Unclamped Inductive Switching Wave



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