



# **Product Specification**

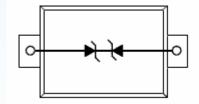
| Domestic Part Number   | LESD3Z5.0CMT1G |
|------------------------|----------------|
| Overseas Part Number   | LESD3Z5.0CMT1G |
| Equivalent Part Number | LESD3Z5.0CMT1G |





#### **General Description**

The LESD3Z5.0CMT1G is designed to protect voltage sensitive components from ESD and transient voltage events. Excellent clamping capability, low leakage, and fast response time, make these parts ideal for ESD protection on designs where board space is at a premium.



#### Applications

- Cellular phones
- Portable devices
- Digital cameras
- Power supplies

#### Features

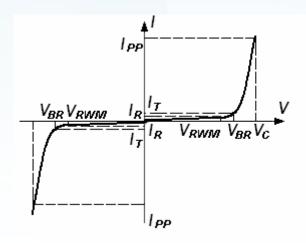
- Small Body Outline Dimensions
- Low Body Height
- Peak Power up to 200 Watts @ 8 x 20 \_s Pulse
- Low Leakage current
- Response Time is Typically < 1 ns
- ESD Rating of Class 3 (> 16 kV) per Human Body Model
- IEC61000-4-2 Level 4 ESD Protection
- IEC61000-4-4 Level 4 EFT Protection

#### Absolute Ratings (T<sub>amb</sub>=25°C)

| Symbol           | Parameter  | Value       | Units |
|------------------|--|-------------|-------|
| P <sub>PP</sub>  | Peak Pulse Power ( $t_p = 8/20 \ \mu \ s$ )        | 200         | W     |
| ΤL               | Maximum lead temperature for soldering during 10s  | 260         | °C    |
| T <sub>stg</sub> | Storage Temperature Range                          | -55 to +155 | °C    |
| T <sub>op</sub>  | Operating Temperature Range                        | -40 to +125 | °C    |
| Tj               | Maximum junction temperature                       | 150         | °C    |
|                  | IEC61000-4-2 (ESD) air discharge contact discharge | 土15<br>土8   | KV    |
|                  | IEC61000-4-4 (EFT)                                 | 40          | Α     |
|                  | ESD Voltage Per Human Body Model                   | 16          | KV    |

#### **Electrical Parameter**

| Symbol           | Parameter   |
|------------------|---|
| I <sub>PP</sub>  | Maximum Reverse Peak Pulse Current                    |
| Vc               | Clamping Voltage @ IPP                                |
| V <sub>RWM</sub> | Working Peak Reverse Voltage                          |
| I <sub>R</sub>   | Maximum Reverse Leakage Current @<br>V <sub>RWM</sub> |
| Ι <sub>Τ</sub>   | Test Current  |
| V <sub>BR</sub>  | Breakdown Voltage @ I <sub>T</sub>                    |



Electrical Characteristics Ratings at 25°C ambient temperature unless otherwise specified.VF = 0.9V at IF = 10mA

| Device         | V <sub>RWM</sub><br>(V) | I <sub>R</sub> (uA)<br>@ V <sub>RWM</sub> | V <sub>BR</sub> (V)@ I <sub>T</sub><br>(Note 1) | Ι <sub>τ</sub> | V <sub>C</sub> (V)<br>@ I <sub>PP</sub> =5 A* | V <sub>C</sub> (V)<br>@ Max I <sub>PP</sub> * | І <sub>РР</sub><br>(А)* | Р <sub>РК</sub><br>(W)* | C<br>(pF) |
|----------------|-------------------------|---|---|----------------|---|---|-------------------------|-------------------------|-----------|
|                | Max                     | Max                                       | Min   | mA             | Тур   | Max   | Мах                     | Мах                     | Тур       |
| LESD3Z5.0CMT1G | 5.0                     | 1   | 5.6   | 1.0            | 11.6  | 18.6  | 9.4                     | 174                     | 25        |

\*Surge current waveform per Figure 1.

1.  $V_{BR}$  is measured with a pluse test current  $I_T$  at an ambient temperature of  $25^\circ\! {\rm C}$  .

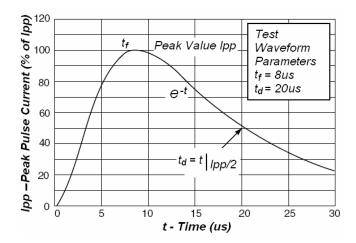
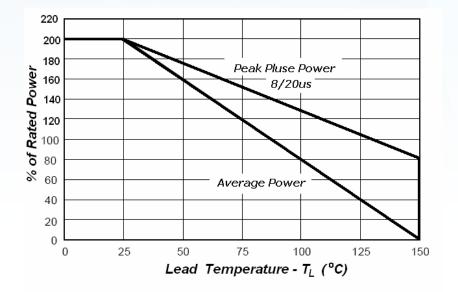


Fig1. Pulse Waveform





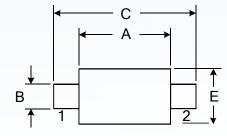
#### **Application Note**

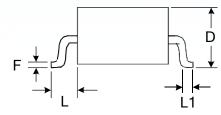
Electrostatic discharge (ESD) is a major cause of failure in electronic systems. Transient Voltage Suppressors (TVS) are an ideal choice for ESD protection. They are capable of clamping the incoming transient to a low enough level such that damage to the protected semiconductor is prevented.

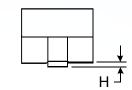
Surface mount TVS offers the best choice for minimal lead inductance. They serve as parallel protection elements, connected between the signal lines to ground. As the transient rises above the operating voltage of the device, the TVS becomes a low impedance path diverting the transient current to ground. The LESD3Z5.0CMT1G is the ideal board evel protection of ESD sensitive semiconductor components.

The tiny SOD-323 package allows design flexibility in the design of high density boards where the space saving is at a premium. This enables to shorten the routing and contributes to hardening against ESD.

**Outline Drawing - SOD-323** 

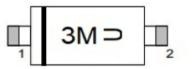






| DIMENSIONS |           |              |        |       |  |
|------------|-----------|--------------|--------|-------|--|
| SYMBOL     | MILLIN    | <b>IETER</b> | INCHES |       |  |
| OTWIDOL    | MIN       | MAX          | MIN    | MAX   |  |
| А          | 1.600     | 1.800        | 0.063  | 0.071 |  |
| В          | 0.250     | 0.350        | 0.010  | 0.014 |  |
| С          | 2.500     | 2.700        | 0.098  | 0.106 |  |
| D          |           | 1.000        |        | 0.039 |  |
| E          | 1.200     | 1.400        | 0.047  | 0.055 |  |
| F          | 0.080     | 0.150        | 0.003  | 0.006 |  |
| L          | 0.475 REF |              | 0.019  | REF   |  |
| L1         | 0.250     | 0.400        | 0.010  | 0.016 |  |
| Н          | 0.000     | 0.100        | 0.000  | 0.004 |  |

## Marking



## **Ordering information**

| Order code     | Package | Baseqty | Delivery mode |
|----------------|---------|---------|---------------|
| LESD3Z5.0CMT1G | SOD-323 | 3000    | Tape and reel |



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