















**ESD** 

TVS

MOS

LDO

Diode

Sensor

DC-DC

# **Product Specification**

Domestic Part Number	SK54C
Overseas Part Number	SK54C
▶ Equivalent Part Number	SK54C





Surface Mount Schottky Barrier Rectifier Reverse Voltage - 40V Forward Current - 5.0A

#### **FEATURES**

- Metal silicon junction, majority carrier conduction
- For surface mounted applications
- · Low power loss, high efficiency
- High forward surge current capability
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications

#### **MECHANICAL DATA**

· Case: SMC

• Terminals: Solderable per MIL-STD-750, Method 2026

Approx. Weight: 0.22g / 0.0077oz

#### **PINNING**

PIN	DESCRIPTION
1	Cathode
2	Anode



#### Absolute Maximum Ratings and Electrical characteristics

Ratings at 25  $^{\circ}$ C ambient temperature unless otherwise specified. Single phase, half wave, 60Hz resistive or inductive load, for capacitive load, derate by 20  $^{\circ}$ 

Parameter	Symbols	SK54C	Units
Maximum Repetitive Peak Reverse Voltage	$V_{RRM}$	40	V
Maximum RMS voltage	V <sub>RMS</sub>	28	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	40	V
Maximum Average Forward Rectified Current	I <sub>F(AV)</sub>	5.0	А
Peak Forward Surge Current,8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I <sub>FSM</sub>	175	А
Max Instantaneous Forward Voltage at 5 A	V <sub>F</sub>	0.55	V
Maximum DC Reverse Current $T_a = 25^{\circ}$ C at Rated DC Reverse Voltage $T_a = 100^{\circ}$ C	I <sub>R</sub>	1.0 50	mA
Typical Junction Capacitance (1)	Cj	600	pF
Typical Thermal Resistance (2)	$R_{\theta JA}$	35	°C/W
Operating Junction Temperature Range	Tj	-55 ~ +125	°C
Storage Temperature Range	$T_{stg}$	-55 ~ +150	°C

<sup>(1)</sup> Measured at 1 MHz and applied reverse voltage of 4 V D.C

<sup>(</sup> 2 ) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



Fig.1 Forward Current Derating Curve

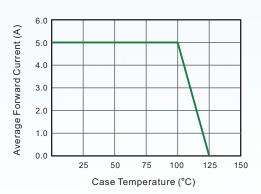


Fig.2 Typical Reverse Characteristics

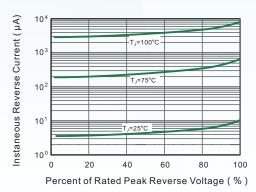


Fig.3 Typical Forward Characteristic

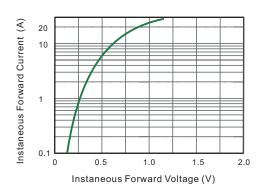


Fig.4 Typical Junction Capacitance

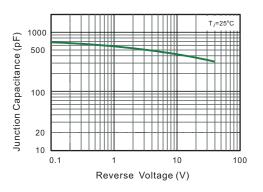


Fig.5 Maximum Non-Repetitive Peak Forward Surage Current

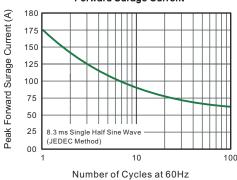
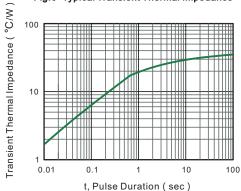


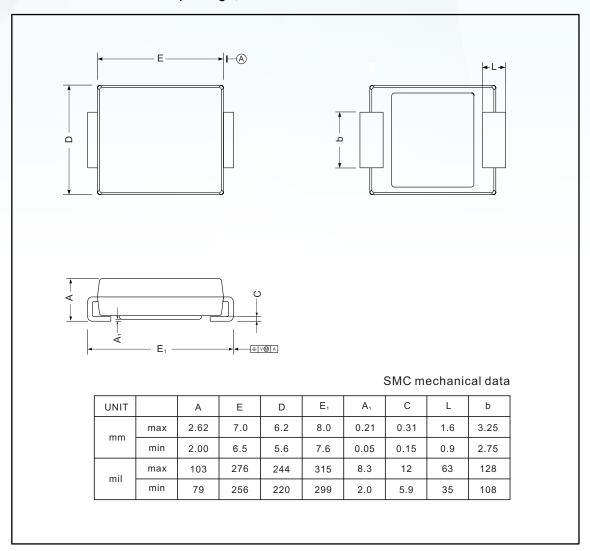
Fig.6- Typical Transient Thermal Impedance



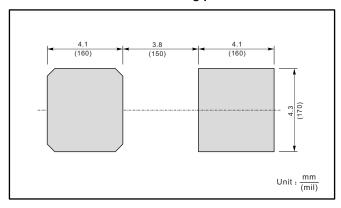


## PACKAGE OUTLINE

Plastic surface mounted package; 2 leads



# The recommended mounting pad size



## Marking

Type number	Marking code
SK54C	SS54



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