

EVVOSEMI[®]

THINK CHANGE DO



ESD



TVS



MOS



LDO



Diode



Sensor



DC-DC

Product Specification

▶ Domestic	Part Number	IRFS23N20D
▶ Overseas	Part Number	IRFS23N20D
▶ Equivalent	Part Number	IRFS23N20D

EV is the abbreviation of name EVVO

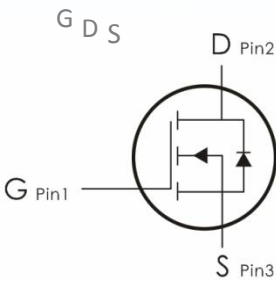
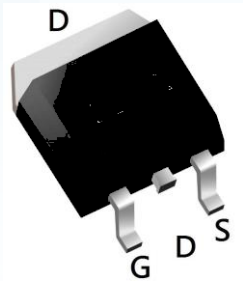
Description:

This N-Channel MOSFET uses advanced Planar technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=200V, I_D=52A, R_{DS(ON)}<60m\ \Omega$ @ $V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra low $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.

TO263 Pin Configuration



Absolute Maximum Ratings: ($T_C=25^{\circ}C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage ¹	200	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	52	A
	Continuous Drain Current- $T_C=100^{\circ}C$	28	
I_{DM}	Pulsed Drain Current ²	70	
P_D	Power Dissipation	125	W
E_{AS}	Single pulse avalanche energy	1200	mJ
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to 150	$^{\circ}C$

Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{\theta JC}$	Thermal Resistance, Junction to Case	1.0	$^{\circ}C/W$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	62	$^{\circ}C/W$

Electrical Characteristics: ($T_C=25^{\circ}\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Sourctce Breakdown Voltage	V _{GS} =0V, I _D =250 μ A	200	---	---	V
I _{DSS}	Zero Gate Voltage Drain Current	V _{GS} =0V, V _{DS} =200V	---	---	1	μ A
		V _{GS} =0V, V _{DS} =160V, T _J =125 °C	---	---	100	μ A
I _{GSS}	Gate-Source Leakage Current	V _{GS} =± 20V, V _{DS} =0A	---	---	± 100	nA
On Characteristics						
V _{GS(th)}	GATE-Source Threshold Voltage	V _{GS} =V _{DS} , I _D =250 μ A	2	---	4	V
R _{DS(ON)}	Drain-Source On Resistance ⁴	V _{GS} =10V, I _D =20A	---	50	60	m Ω
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, f=1MHz	---	2799	3699	pF
C _{oss}	Output Capacitance		---	304	399	
C _{rss}	Reverse Transfer Capacitance		---	109	149	
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DS} =100V, I _D =20A, R _G =3.9 Ω , V _{GS} =10V	---	19	---	ns
t _r	Rise Time		---	29	---	ns
t _{d(off)}	Turn-Off Delay Time		---	64	---	ns
t _f	Fall Time		---	24	---	ns
Q _g	Total Gate Charge	V _{DD} =100V, I _D =20A V _{GS} =0 to 10V	---	96	120	nc
Q _{gs}	Gate-Source Charge		---	13	---	nc
Q _{gd}	Gate-Drain “Miller” Charge		---	38	---	nc
Drain-Source Diode Characteristics						
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =40A	---	---	1.5	V
I _S	Continuous Drain Curren ⁴	VD=VG=0V	---	---	52	A
I _{SM}	Pulsed Drain Current ⁴		---	---	70	A
T _{rr}	Reverse Recovery Time	I _F =20A, V _{GS} =0V, dI/dt=100A/us	---	280	---	ns
Q _{rr}	Reverse Recovery Charge	I _F =20A, V _{GS} =0V, dI/dt=100A/us	---	420	---	nc

Notes:

1. $T_J = +25^{\circ}\text{C}$ to $+150^{\circ}\text{C}$
2. Repetitive rating; pulse width limited by maximum junction temperature.
3. $ISD = 20A$ di/dt $< 100 A/\mu s$, $V_{DD} < BV_{DSS}$, $T_J = +150^{\circ}\text{C}$.
4. Pulse width $\leq 380\mu s$; duty cycle $\leq 2\%$

Typical Characteristics: ($T_C = 25^{\circ}\text{C}$ unless otherwise noted)

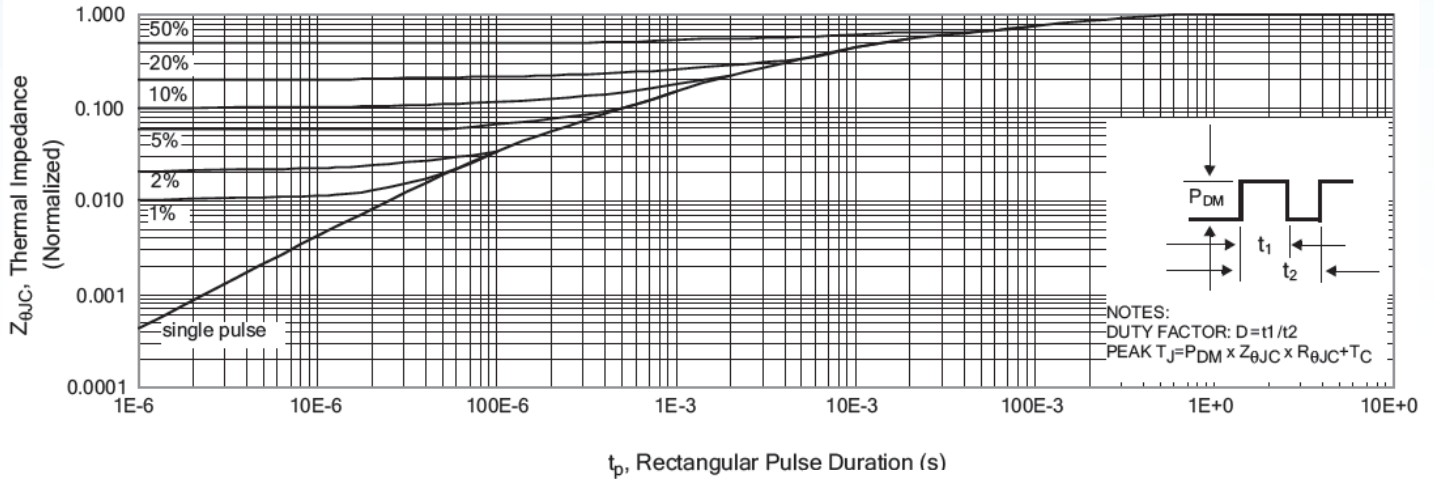


Figure 1. Maximum Effective Thermal Impedance, Junction-to-Case

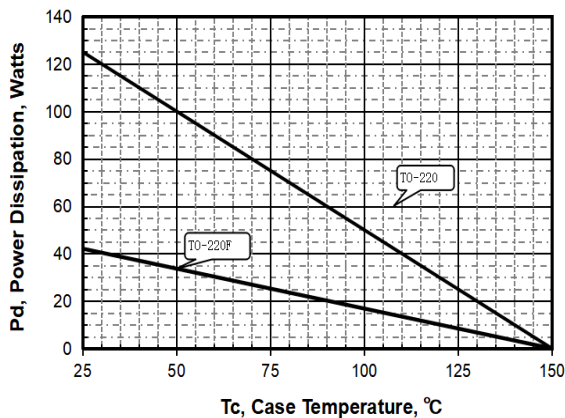


Figure 2. Max. Power Dissipation vs Case Temperature

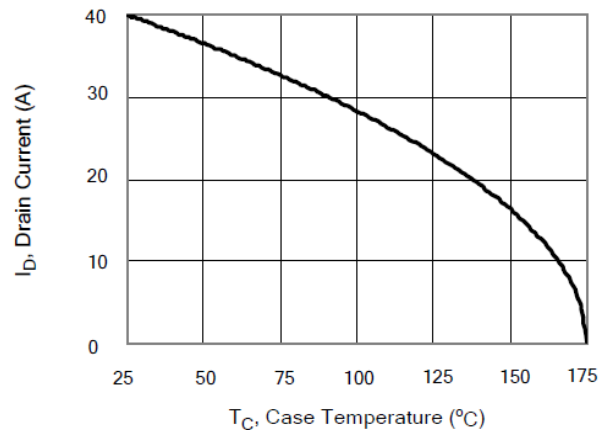


Figure 3. Maximum Continuous Drain Current vs Case Temperature

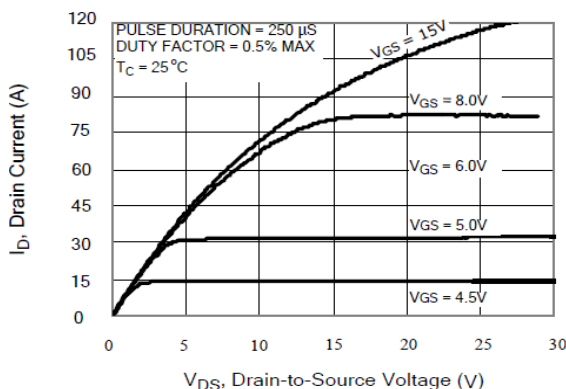


Figure 4. Typical Output Characteristics

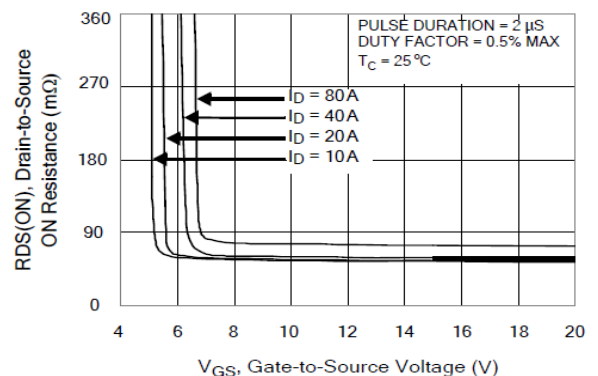
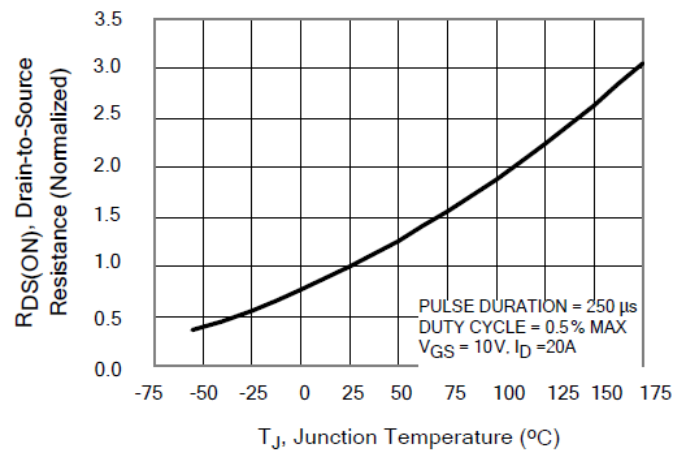
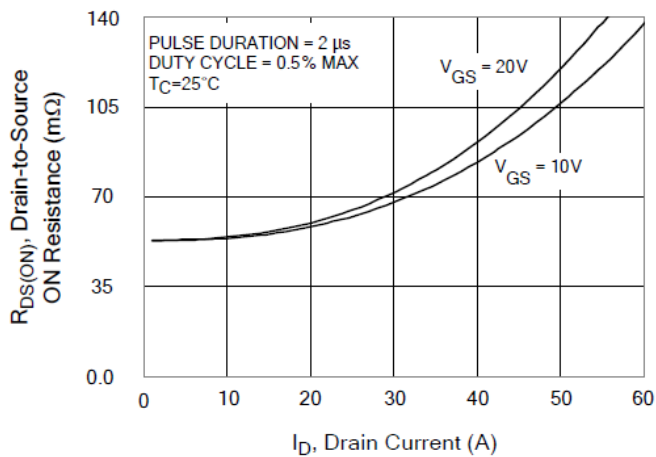
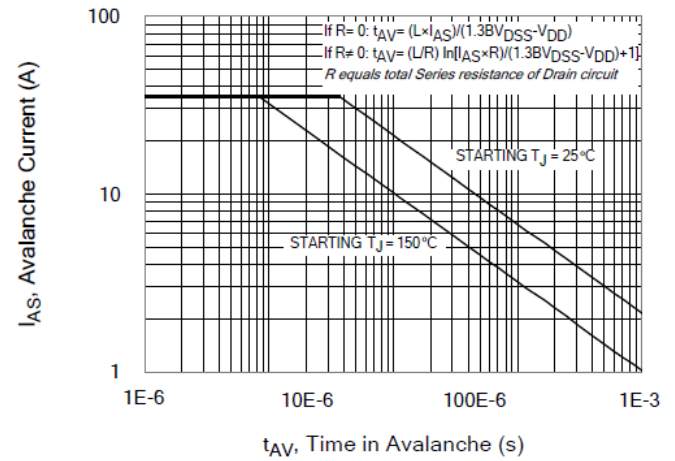
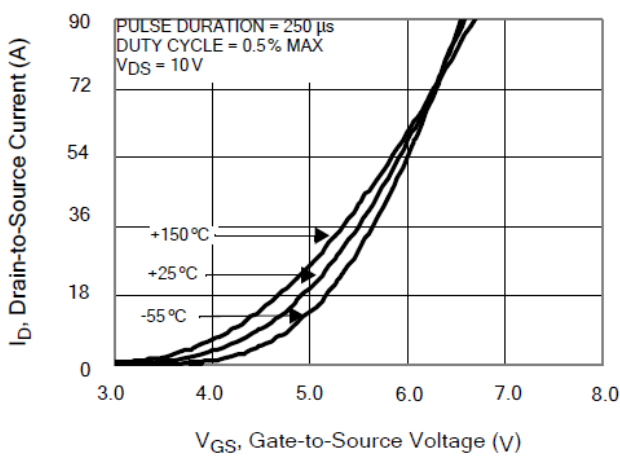
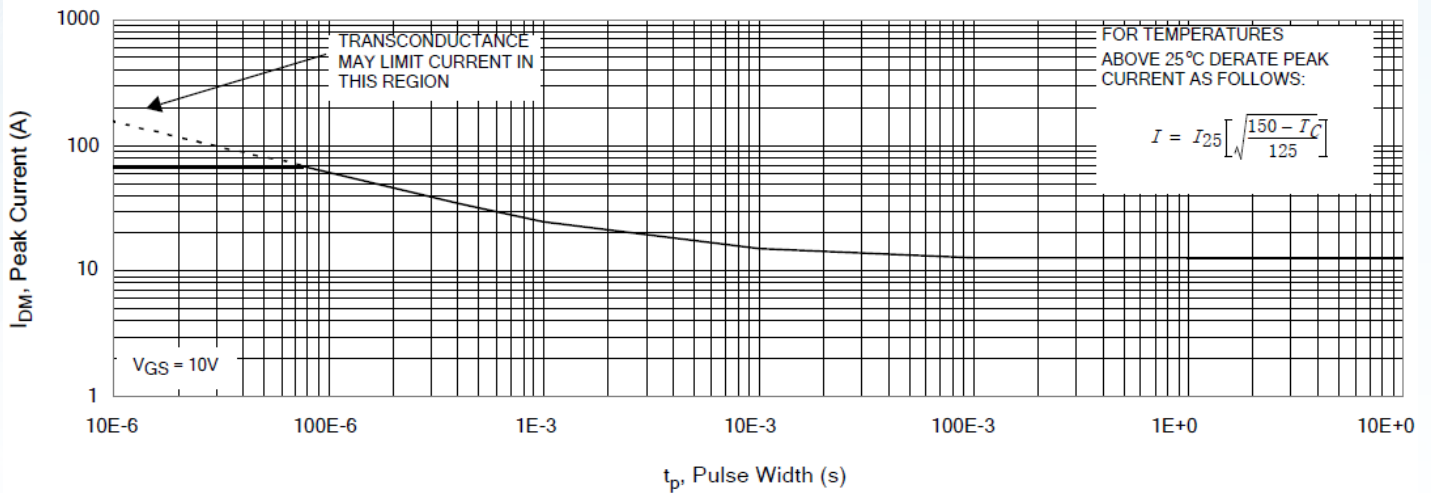


Figure 5. Typical Drain-to-Source ON Resistance vs Gate Voltage and Drain Current



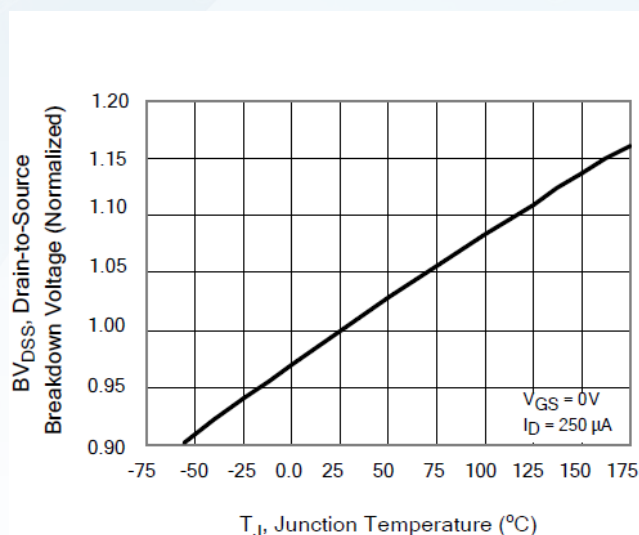


Figure 11. Typical Breakdown Voltage vs Junction Temperature

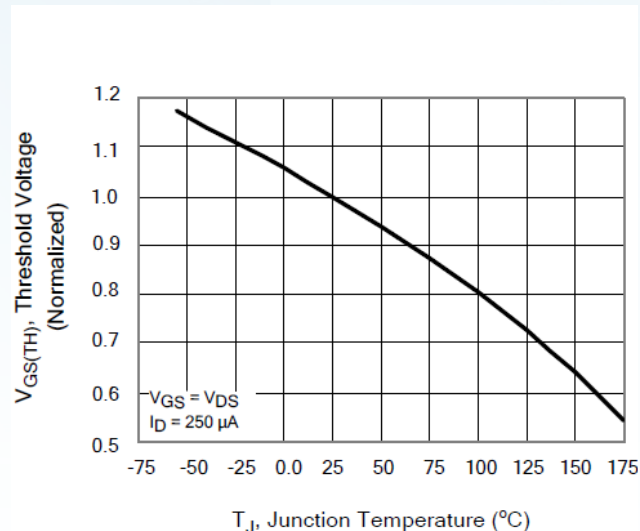


Figure 12. Typical Threshold Voltage vs Junction Temperature

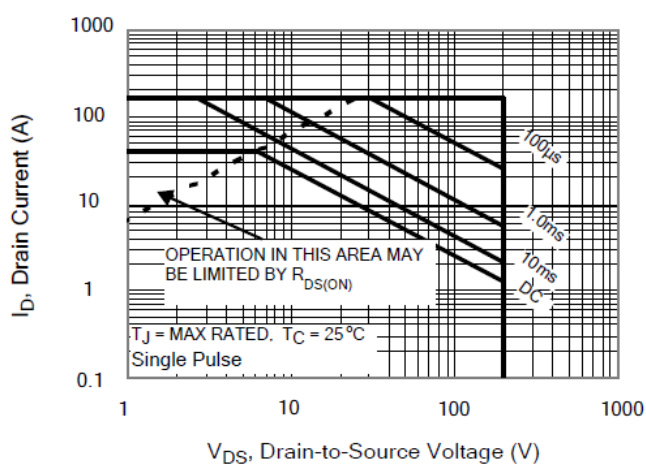


Figure 13. Maximum Forward Bias Safe Operating Area

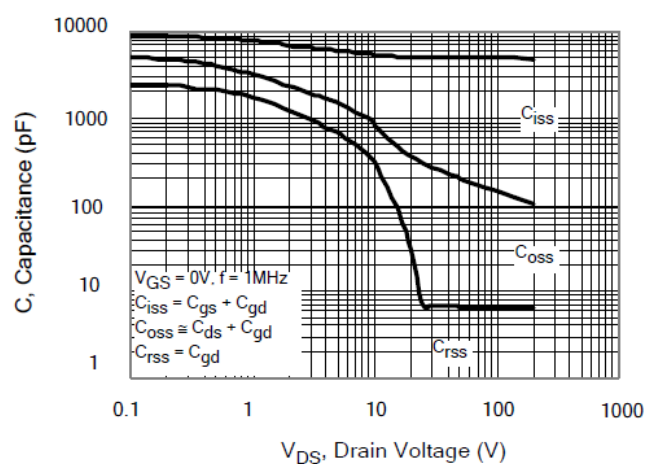


Figure 14. Typical Capacitance vs Drain-to-Source Voltage

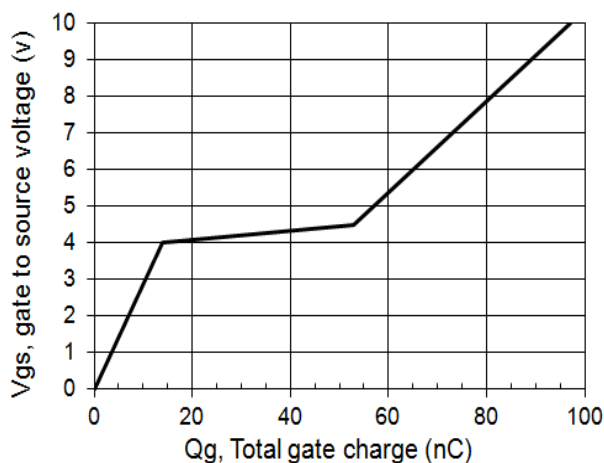


Figure 15. Typical Gate Charge

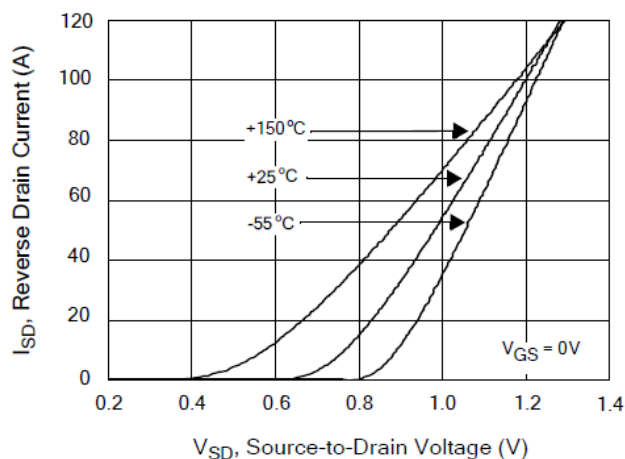
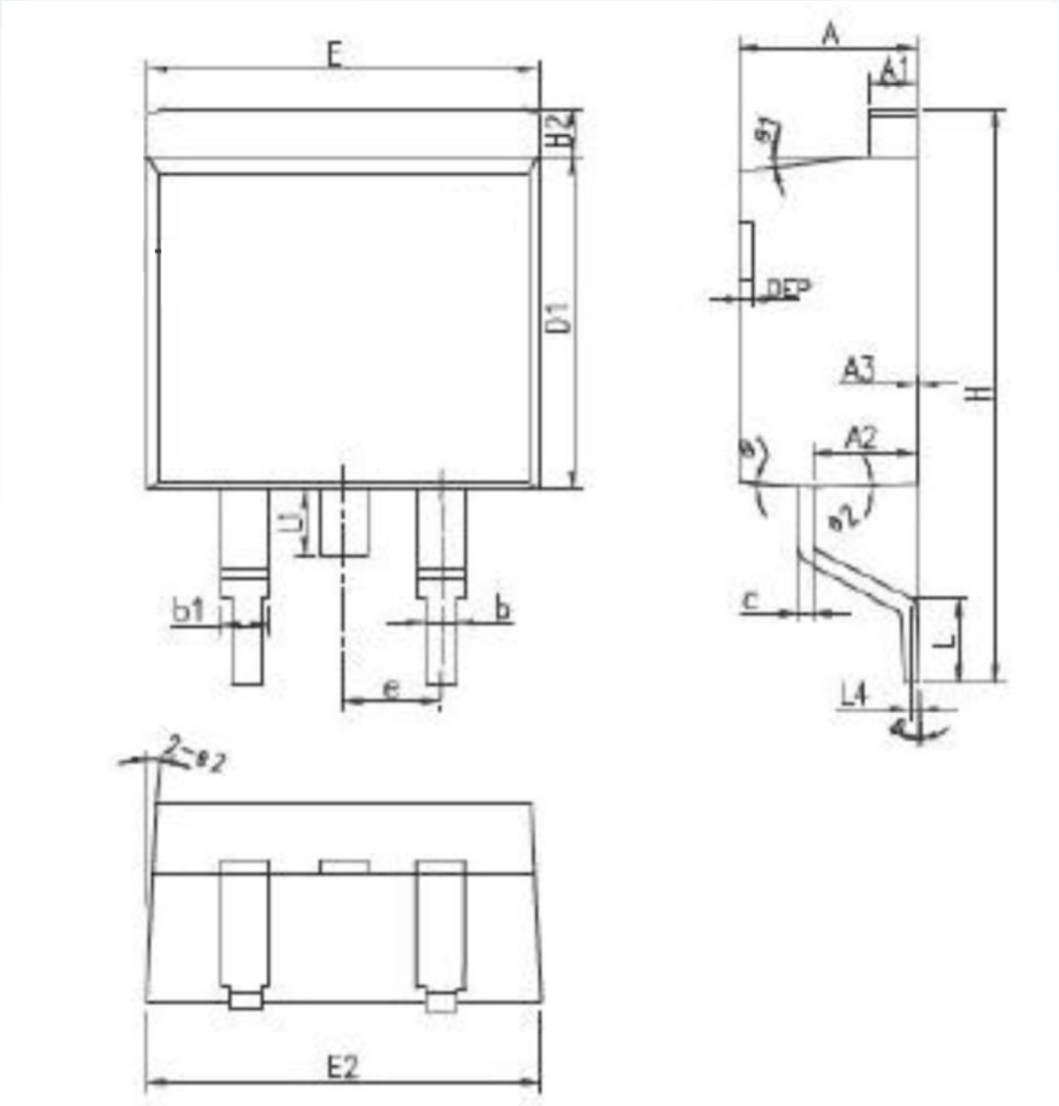


Figure 16. Typical Body Diode Transfer Characteristics

Package Mechanical Data TO-263



Symbol	Inches			Millimeters		
	Min	Nom	Max	Min	Nom	Max
A	4.40	4.57	4.57	0.173	0.180	0.185
A1	1.22	1.27	1.27	0.048	0.050	0.052
A2	2.59	2.69	2.69	0.102	0.106	0.110
A3	0.00	0.10	0.10	0.000	0.004	0.008
b	0.77	0.813	0.813	0.030	0.032	0.035
b1	1.20	1.270	1.270	0.047	0.050	0.054
c	0.34	0.381	0.381	0.013	0.015	0.019
D1	8.60	8.70	8.99	0.339	0.343	0.354
E	10.00	10.16	10.16	0.394	0.400	0.404
E2	10.00	10.10	10.10	0.394	0.398	0.402
e	2.54BSC			0.100BSC		
H	14.70	15.10	15.50	0.579	0.594	0.610
H2	1.17	1.27	1.40	0.046	0.050	0.055
L	2.00	2.30	2.60	0.079	0.091	0.102
L1	1.45	1.55	1.70	0.057	0.061	0.067
L4	0.25BSC			0.010BSC		
θ	0°	5°	8°	0°	5°	8°
θ1	5°	7°	9°	5°	7°	9°
θ2	1°	3°	5°	1°	3°	5°
DEP	0.05	0.10	0.20	0.002	0.004	0.008

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