















**ESD** 

TVS

MOS

LDO

Diode

Sensor

DC-DC

# **Product Specification**

Domestic Part Number	HEF4011BT
<ul><li>Overseas Part Number</li></ul>	HEF4011BT
▶ Equivalent Part Number	HEF4011BT





### 产品简介

HEF4011BT 是一款采用 CMOS 技术设计的低功耗宽范围工作电压的 2 输入与非门集成电路。它内部集成 4组相互独立的 2 输入与非门电路,具有高抗干扰能力和驱动能力。

#### 产品特点

-低输入电流: I<sub>IN</sub>≤1uA, @ V<sub>IN</sub>=VDD=15V, Ta=25℃

-低静态功耗: I<sub>DD</sub>≤6uA, @VDD=15V, Ta=25℃

-宽工作电压范围: 3.0V to 15.0V

-封装形式: DIP14 、SOP14

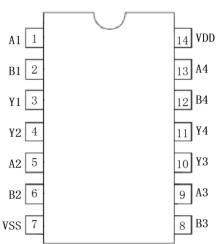
#### 产品用途

数字逻辑驱动 无线门铃 工控应用 其它应用领域

#### 封装形式和管脚功能定义

管脚序号	管脚	管脚序号	管脚
DIP14/SOP14	定义	DIP14/S0P14	定义
1	A1	14	VDD
2	B1	13	В4
3	Y1	12	A4
4	Y2	11	Y4
5	A2	10	Ү3
6	В2	9	В3
7	VSS	8	А3

#### DIP14/S0P14



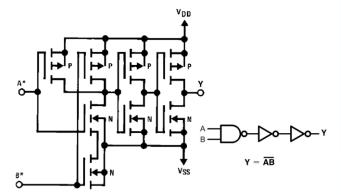
#### 极限参数

参数	符号	极限值	单位
电源电压	$V_{DD}$	-0.5-18	V
输入电压	$V_{IN}$	-0. 5+VSS-V <sub>DD</sub> +0. 5V	V
功耗	$P_{\mathrm{D}}$	500	mW
工作温度	$T_{A}$	0-70	${\mathbb C}$
存储温度	Ts	-65-150	${\mathbb C}$
引脚焊接温度	Tw	260, 10s	$^{\circ}$

注: 极限参数是指无论在任何条件下都不能超过的极限值。如果超过此极限值,将有可能造成产品劣化等物理性损伤; 同时在接近极限参数下,不能保证芯片可以正常工作。



### 原理逻辑图



### 真值表

Inp	uts	Output
Α	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

H = High Logic Level

L = Low Logic Level

### 推荐工作条件

项目	符号	最小值	典型值	最大值	单位
工作电压	$V_{DD}$	2. 5		15	V
输入输出电压	V <sub>IN</sub> , Vout	0		VDD	V
工作温度	$T_{A}$	0		60	${\mathbb C}$

### 电学特性

直流电学特性: T<sub>A</sub>=25℃

符号	项目	测证	<b>代条件</b>	VDD (V)	最小值	典型值	最大值	单位
	<b>宣也亚去</b> 热		Vo= 0.5V	5	3. 5			V
$V_{\mathrm{IH}}$	高电平有效	$ \operatorname{I}_{O}  \leq 1$ uA	$V_O = 1V$	10	7.0			V
输入电压		Vo= 1.5V	15	11.0			V	
	V <sub>IL</sub> 低电平有效		Vo= 4.5V	5			1.5	V
$V_{\mathrm{IL}}$		$ \operatorname{I}_{O}  \leq 1$ uA	Vo= 9V	10			3.0	V
	输入电压		Vo=13.5V	15			4.0	V
				5	4. 95			V
$V_{OH}$	V <sub>OH</sub> 高电平输出电压	I <sub>OU</sub> -	10	9. 95			V	
		15	14. 95			V		
			5			0.05	V	
$V_{OL}$	低电平输出电压	I <sub>OU</sub> -	I <sub>OUT</sub>  <1uA				0.05	V
			15			0.05	V	
$\mathbf{I}_{IN}$	输入电流	$V_{IN} = VDI$	15		0.01	1.0	uA	
		$V_O =$	4. 6V	5		-1.0	-0.5	mA
$I_{OH}$	高电平输出电流	$V_O =$	10		-2.1	-1.3	mA	
		$V_{O}=$	15		-8.0	-3.4	mA	
		$V_O =$	Vo= 0.4V		0.5	2.2		mA
$I_{OL}$	低电平输出电流	$V_O =$	10	1.3	5 <b>.</b> 1		mA	
		Vo=	15	3. 4	19		mA	
						0.1	4	uA
${ m I}_{\sf DD}$	工作电流	V <sub>IN</sub> =VDD or VSS		10		0.1	5	uA
				15		0.1	6	uA

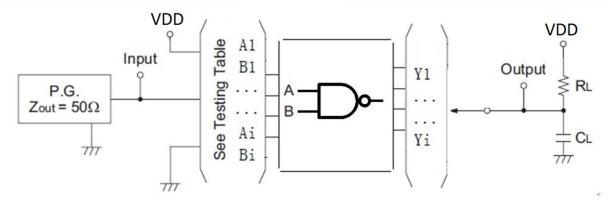


**交流电学特性:** Ta=25℃, R<sub>L</sub>=197k, C<sub>L</sub>=51pF 见测试方法。

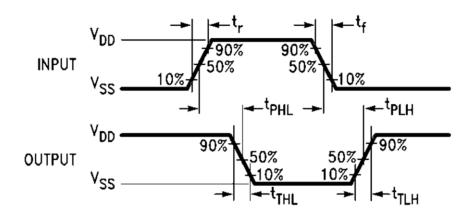
项目	符号	测试条件	最小值	典型值	最大值	单位
最大传输延迟时间 A、B to Y	t <sub>PHL</sub>	VDD=5V		62		ns
	$t_{PLH}$	V 00-5 V		55		ns
	$t_{\mathtt{PHL}}$	VDD=10V		35		ns
	$t_{\scriptscriptstyle{PLH}}$	VDD-10V		35		ns
	$t_{\mathtt{PHL}}$	VDD=15V		30		ns
	$t_{PLH}$	157–עעיי		28		ns

### 测试方法

#### 1、测试接线图



#### 2、波形测量示意图

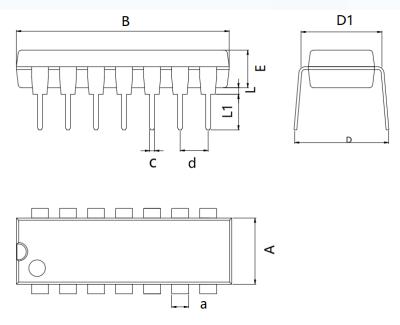


- 注: 1、See Testing Table 指交流电学特性表中相应测试项目;
  - 2、CL 电容为外接贴片电容(0603),靠近输出管脚接入,电容地靠近芯片 VSS;
  - 3、Input:端口输入电平,f=1MHz,D=50%方波,t<sub>r</sub>=t<sub>f</sub>≤20ns;
  - 4、Output: Y端输出测试。



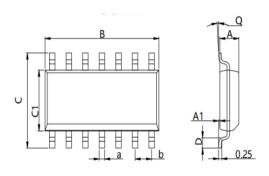
### 封装信息

# DIP14



Dimensions In Millimeters(DIP14)										
Symbol:	A	В	D	D1	Е	L	L1	a	С	d
Min:	6. 10	18. 94	8. 40	7. 42	3. 10	0. 50	3. 00	1.50	0.40	0 F4 PGG
Max:	6. 68	19. 56	9.00	7.82	3. 55	0.70	3. 60	1. 55	0. 50	2. 54 BSC

# S0P14



Dimensions In Millimeters (SOP14)									
Symbol:	A	A1	В	С	C1	D	Q	a	b
Min:	1. 35	0.05	8. 55	5.80	3.80	0.40	0°	0.35	1 97 DCC
Max:	1.55	0. 20	8. 75	6. 20	4. 00	0.80	8°	0. 45	1.27 BSC



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